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<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L14</u>	L13 and l12 and l11	0	<u>L14</u>
<u>L13</u>	battery near6 (bind\$4 or bound)	672	<u>L13</u>
<u>L12</u>	(latch or register or non-volatile) near4 (bind\$4 or bound)	1189	<u>L12</u>
<u>L11</u>	data near4 (bind\$4 or bound)	7009	<u>L11</u>
<u>L10</u>	L9 and l8	3	<u>L10</u>
<u>L9</u>	pump near4 (power or voltage or current or charge)	42550	<u>L9</u>
<u>L8</u>	L7 and ((module or DIMM or SIMM) with (planar or motherboard))	18	<u>L8</u>
<u>L7</u>	L6 and l4	178	<u>L7</u>
<u>L6</u>	L5 same l3	957	<u>L6</u>
<u>L5</u>	(latch or register or non-volatile) near6 (set\$4 or reset\$4)	78619	<u>L5</u>
<u>L4</u>	L3 and l1	3441	<u>L4</u>
<u>L3</u>	battery with (power or current or voltage)	126006	<u>L3</u>
<u>L2</u>	battery with (power or current or volatge)	115472	<u>L2</u>

L1 (latch or register or non-volatile) with (secur\$4 or bind\$4 or protect\$4 or
restrain\$4 or restrict\$4 or bound\$4)

48377 L1

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1 **Battery-Driven System Design: A New Frontier in Low Power Design** **98%**



Kanishka Lahiri , Sujit Dey , Debashis Panigrahi , Anand Raghunathan

Proceedings of the 2002 conference on Asia South Pacific design automation/VLSI Design January 2002

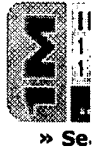
As an increasing number of electronic systems are powered by batteries, battery life becomes a primary design consideration. Maxiimizing battery life requires system designers to develop an understanding of the capabilities and limitations of the batteries that power such systems, and to incorporate battery considerations into the system design process. Recent research has shown that, the amount of energy that can be supplied by a given battery varies significantly, depending on how the energy i ...

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(charg\$ <near/4> (pump or circuit)) and (battery <near/4>

☐ Check to search within this result set**Results Key:****JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**1 Design and implementation of a novel Ni-Cd battery charger***Guan-Chyun Hsieh; Jung-Chien Li; Jia-Chwen Hwang;*

Industrial Electronics, Control, Instrumentation, and Automation, 1992. 'Power Electronics and Motion Control', Proceedings of the 1992 International Conference on , 9-13 Nov. 1992

Pages:1163 - 1167 vol.3

[\[Abstract\]](#)[\[PDF Full-Text \(288 KB\)\]](#)**IEEE CNF**
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```
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<near/4> signal)
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(bind$ <near/4> (latch or
register or flip-flop))and
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```

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